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TITLE

Memory Channel Storage: Unleashing Application Performance

ABSTRACT

Moving flash onto the memory bus has unlocked unprecedented storage performance. Ultimately, however, application-level benefits are the true barometers for success. With applications ranging from Database to Virtualization to Analytics to Networking, ISVs and enterprises have discovered the business-critical advantages offered by the Memory Channel Storage (MCS) platform. By co-locating dynamic and persistent memory, MCS provides the foundation for the next-generation of application acceleration

BIOGRAPHY

Maher Amer is Chief Technology Officer at Diablo Technologies Corporation. Maheer has over 15 years of experience in the field of ASIC development and system architecture. He has been with Diablo since 2005 starting as Senior ASIC architect. Since then he held a series of technical and management positions. Maheer was a key contributor to the ASIC implementation and architecture of Diablo's Advanced Memory Buffer (AMB). He also led Diablo System Architecture team in defining the only plug-and-play proprietary Load Reduction chipset for DDR3 memory channels. Maheer owns the System Engineering effort within Diablo and maintains key technical relationships with forward thinkers involved in the server and storage industry. Prior to joining Diablo Maheer was the Digital Prime at IceFyre semiconductor, responsible for the development of an advanced 802.11a wireless chipset. With strong DSP knowledge and solid theoretical background Maheer was essential in the implementation and architecture of the IceFyre wireless chipset. Maheer was also part of Extreme Packet Devices; a startup that was successfully acquired by PMC-Sierra. Maheer started his career as an analogue designer at Mosaid Technologies designing DRAM and SRAM memory arrays. Maheer holds a Bachelor Degree in Electrical Engineering with Honors from McGill University where he specialized in Communication and VLSI.