Rob Peglar, Vice President of Advanced Storage, Micron Technology, Inc.

TITLE
Always Where you Need it: New Approaches to Traditional Device Memory and Storage

ABSTRACT

This talk will discuss the architectural aspects of using non-volatile memory to support both existing applications and to enable new applications. The best way to reduce bottlenecks in processing is to re-architect the relationship between persistent data and processors, and thereby enable workloads and applications, which were previously out-of-scope in terms of the time and expense required.

BIOGRAPHY

Rob Peglar is Vice President of Advanced Storage at Micron Technology, Inc. A 38-year industry veteran and published author, he leads efforts in advanced storage systems strategy, leads executive-level planning for the Storage Business Unit with key customers and partners worldwide, and defines future storage offering portfolios incorporating Micron technology. He serves on Board of Directors of the SNIA and is the former co-chair of the SNIA Analytics and Big Data Committee and the SNIA Tutorials, and former director in the SNIA Solid State Storage Initiative. He serves as an advisor to the Flash Memory Summit. He has extensive experience in data management and analysis, high-performance computing, non-volatile memory, distributed cluster architectures, filesystems, I/O performance optimization, cloud storage, replication and archiving strategy, networking protocols, storage virtualization, and is a sought-after speaker and panelist at leading storage and cloud-related seminars and conferences worldwide. He was named an EMC Elect in 2014 and 2015. He was one of 25 senior executives worldwide selected for the CRN 'Storage Superstars' Award in 2010.

Prior to joining Micron in February 2015, Mr. Peglar was Chief Technology Officer, Americas at EMC Isilon for four years. Prior to EMC, he was a Senior Fellow and Vice President of Technology at Xiotech for eleven years. Prior to Xiotech, he held key technology specialist and engineering leadership positions over a ten-year period at StorageTek and its subsidiary, Network Systems Corporation. Prior to StorageTek, he held engineering development and product management positions for a decade at Control Data and its supercomputer division, ETA Systems.

Mr. Peglar holds the B.S. degree in Computer Science from Washington University, St. Louis Missouri, and performed graduate work at Washington University's Sever Institute of Engineering. His research background includes memory optimization, distributed systems, I/O performance analysis, queuing theory, parallel systems architecture and OS design, file systems & storage networking protocols, clustering algorithms and virtual systems communication.